

WHT Two Chip Mosaic Camera:

Wiring internal to cryostat

		'X'	'Y'	'X'	'Y'
	cryo	chip 1	chip 2	chip 1	chip 2
CCD Sig	20-41	21 way	21 way	15 way	15 way
P1B	H		15		
P1A	g	15			
P2B	P		14		
P2A	K	14			
P3B	k		16		
P3A	M	16			
H1A	G	5,8			
H1B	R		5,8		
DGA(V4A)	j	2,10			
DGB(V4B)	b		2,10		
H2A	J	4,7			
H2B	N		4,7		
DUMP(ABD)	h			1,8	1,8
H3A	L	6			
H3B	f		6		
SWLA	F	1			
SWRA	S	11			
SWLB	n		1		
SWRB	e		11		
RLA	E	3			
RRA	T	9			
RLB	p		3		
RRB	Z		9		
SUBA	Y	13		13	
OG2	D			3,6	3,6
OG1	m	12,21	12,21		
RDLA	C			2	
RDRA	V			7	
RDLB	r				2
RDRB	X				7
ODLA	B			9	
OSLA	A			10	
ODRB	U				15
OSRB	W				14
ODLB	q				9
OSLB	s				10
ODRA	c			15	
OSRA	a			14	
SUBB	i	<- after thought	13		13
N/C		17,18,19,20	17,18,19,20	4,5,11,12	4,5,11,12

2 spare pins in the 20-41