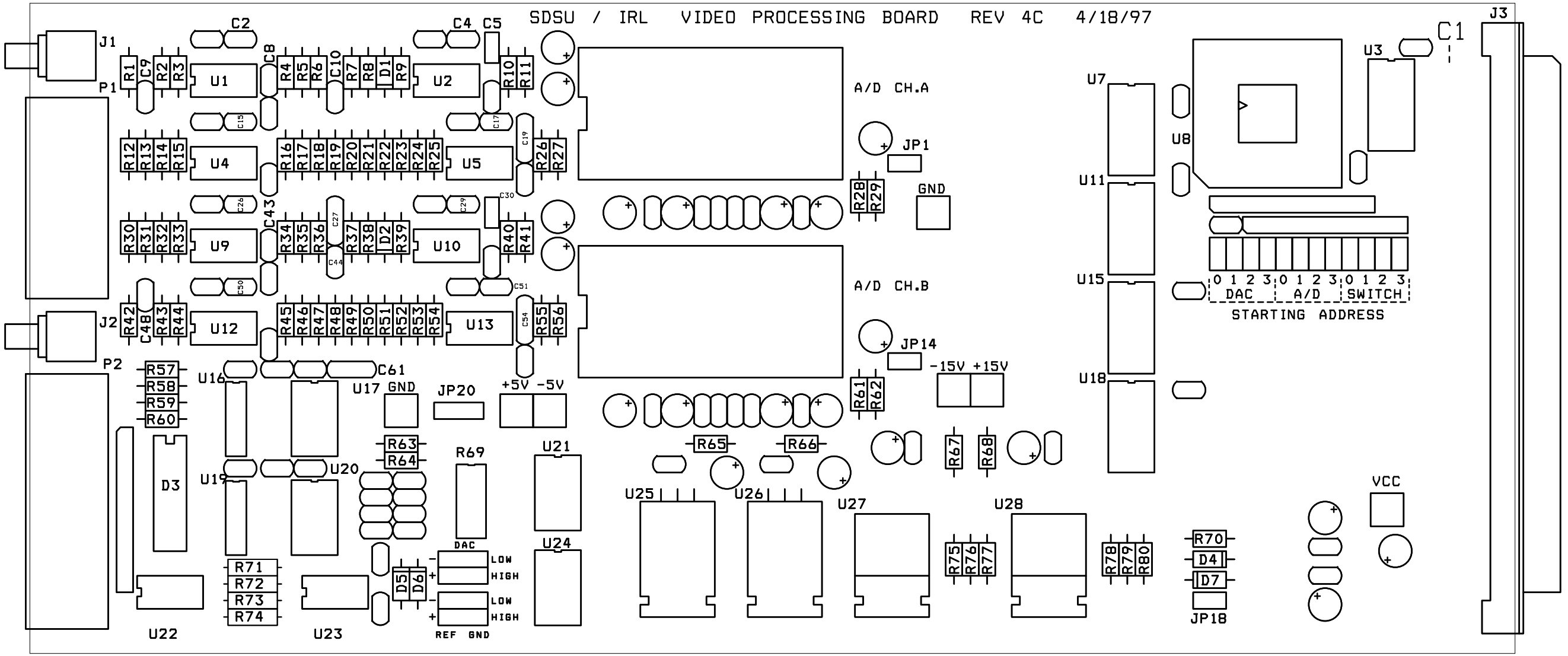
SDSU / IRL VIDEO PROCESSING BOARD REV 4C 4/18/97
J1
P1
J2
P2
J3
C1
C2
C4
C5
C8
C10
C43
C48
C61
U1
U2
U3
U4
U5
U7
U8
U9
U10
U11
U12
U13
U15
U16
U17
U18
U19
U20
U21
U22
U23
U24
U25
U26
U27
U28
A/D CH.A
A/D CH.B
JP1
JP14
JP20
JP18
GND
+5V -5V
-15V +15V
VCC
0 1 2 3 0 1 2 3 0 1 2 3
DAC A/D SWITCH
STARTING ADDRESS
D3
D4
D5
D6
D7
DAC
LOW
HIGH
LOW
HIGH
REF GND
R69