

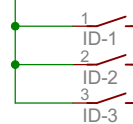
- CON\_LEFT-1) temp
- CON\_LEFT-2) +9v
- CON\_LEFT-3) -9v
- CON\_LEFT-4) 0v
- CON\_LEFT-5) OG
- CON\_LEFT-6) RDI
- CON\_LEFT-7) ODL
- CON\_LEFT-8) RO1L
- CON\_LEFT-9) RO2L
- CON\_LEFT-10) RO3L
- CON\_LEFT-11) ORL
- CON\_LEFT-12) IO1
- CON\_LEFT-13) IO2
- CON\_LEFT-14) IO3

Left 15 way D type

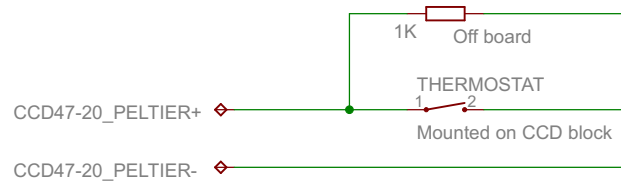
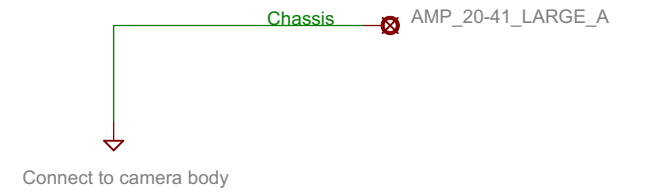
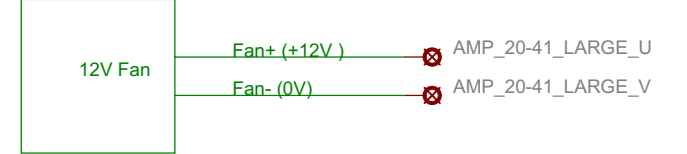
- CON\_RIGHT-1) flash
- CON\_RIGHT-2) +9v
- CON\_RIGHT-3) -9v
- CON\_RIGHT-4) 0v
- CON\_RIGHT-5) DG
- CON\_RIGHT-6) RDR
- CON\_RIGHT-7) ODR
- CON\_RIGHT-8) RO1R
- CON\_RIGHT-9) RO2R
- CON\_RIGHT-10) RO3R
- CON\_RIGHT-11) ORR
- CON\_RIGHT-12) SQ1
- CON\_RIGHT-13) SQ2
- CON\_RIGHT-14) SQ3

Right 15 way D type

ID code selection



- sheath\_l AMP\_20-41\_LARGE\_K
- amp\_l AMP\_20-41\_LARGE\_L
- sheath\_r AMP\_20-41\_SMALL\_F
- amp\_r AMP\_20-41\_LARGE\_M
- temp AMP\_20-41\_SMALL\_B
- +9v AMP\_20-41\_SMALL\_A
- 9v AMP\_20-41\_LARGE\_G
- 0v AMP\_20-41\_LARGE\_H
- OG AMP\_20-41\_LARGE\_N
- RDI/RDR AMP\_20-41\_SMALL\_C
- ODL/ODR AMP\_20-41\_LARGE\_J
- RO1L AMP\_20-41\_SMALL\_E
- RO2L AMP\_20-41\_SMALL\_D
- RO3L AMP\_20-41\_LARGE\_E
- ORL AMP\_20-41\_LARGE\_F
- IO1 AMP\_20-41\_SMALL\_P
- IO2
- IO3
- ID return AMP\_20-41\_LARGE\_T
- ID1 AMP\_20-41\_LARGE\_W
- ID2 AMP\_20-41\_LARGE\_X
- ID3 AMP\_20-41\_LARGE\_Y
- flash AMP\_20-41\_LARGE\_Z
- IO1 AMP\_20-41\_SMALL\_R
- IO2 AMP\_20-41\_SMALL\_S
- IO3 AMP\_20-41\_SMALL\_K
- DG AMP\_20-41\_LARGE\_P
- RDR AMP\_20-41\_SMALL\_G
- ODR AMP\_20-41\_LARGE\_R
- RO1R AMP\_20-41\_SMALL\_I
- RO2R AMP\_20-41\_SMALL\_J
- RO3R/RO3R AMP\_20-41\_SMALL\_H
- ORL/ORR AMP\_20-41\_SMALL\_Q
- SQ1 AMP\_20-41\_SMALL\_M
- SQ2 AMP\_20-41\_SMALL\_N
- SQ3 AMP\_20-41\_SMALL\_T
- Peltier\_status AMP\_20-41\_LARGE\_D
- CCD47-20\_peltier(+5v or +2.5v) AMP\_20-41\_LARGE\_B
- CCD47-20\_peltier(0v) AMP\_20-41\_LARGE\_C
- clock screen AMP\_20-41\_LARGE\_S



TITLE: compact head overview	
Document Number:	REV:
Date: 30/06/2014 13:50:07	Sheet: 1/1