# TEK A/B

| CCD Pin number | cryostat flanged connector | TEK 1024 Function | SDSU Analogue Board | SDSU Clock Board | SDSU front panel |
|---|---|---|---|---|---|
| 49,23 | H | V2u | | 13 | 8 |
| 51,21 | g | V2l | | | 8 |
| 50,22 | P | V3 | | 15 | 9 |
| 48,24 | K | V1u | | 17 | 17 |
| 52,20 | k | V1l | | | 17 |
| 47,25 | M | TGu | | 19 | 3 |
| 53,19 | i | TGl | 11 | | 16 |
| 38 | G | S1a | | 3 | 5 |
| 34 | R | S1b | | | 5 |
| 6 | j | S1c | 3 | | 14 |
| 10 | b | S1d | | | 14 |
| 37 | J | S2a | | 5 | 6 |
| 35 | N | S2b | | | 6 |
| 7 | h | S2c | | | 14 |
| 9 | d | S2d | | | 14 |
| 36 | L | S3ab | | 7 | 7 |
| 8 | f | S3cd | | | 14 |
| | | | | | |
| 46 | F | SWa | | 9 | 1 |
| 26 | S | SWb | | | 1 |
| 54 | n | SWc | | | 14 |
| 18 | e | SWd | | | 14 |
| 40 | E | PhiRa | | 1 | 4 |
| 32 | T | PhiRb | | | 4 |
| 4 | p | PhiRc | | | 14 |
| 12 | Z | PhiRd | | | 14 |
| | | | | | |
| 42,30,14,2 | Y | SUB | 22 | | 12 |
| 55,45,27,17 | D | GND | | | 12 |
| 39,33,11,5 | m | LG | 12 | | 2 |
| 41 | C | RDa | 5 | | 15 |
| 31 | V | RDb | | | 15 |
| 3 | r | RDc | | | 15 |
| 13 | X | RDd | | | 15 |
| | | | | | |
| 44 | B | ODa | 2 | | 11 |
| 43 | A | OSa | | | **A3** |
| 28 | U | ODb | | | 11 |
| 29 | W | OSb | | | **A2** |
| 56 | q | ODc | | | 15 |
| 1 | s | OSc | | | 15 |
| 16 | c | ODd | | | 15 |
| 15 | a | OSd | | | 15 |
| | | | overall shield -> | | 10 |
| | | | | | |
| | | | | | |
| <u>used Outputs and Gates</u> | | | | | |
| | | | | | |
| )D and OS are held to +14 (RD) | | | | | |
| ed RD are kept powered on | | | | | |
| Unused PhiR =10V | | | | | |
| Unused SW =10V | | | | | |
| nused Serial clocks=10V | | | | | |