

**Supplement to**  
**MVME147-0xx**  
**MPU VMEmodule**  
**User's Manual**  
**(MVME147/D3)**

This supplement corrects information on page 1-1 of the *MVME147-0xx MPU VMEmodule User's Manual*, part number MVME147/D3.

- A vertical bar ( | ) in the margin of the replacement page indicates where a change was made.
- The supplement number is shown at the bottom of the new page.

Please insert the attached page according to the following table:

<b>After Existing Page</b>	<b>Insert New Page</b>
1-1/1-2	1-1/1-2

Please place this page behind the title page of the manual as a record of this change.

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## Introduction

This manual provides general information, preparation for use and installation, operating instructions, and functional description for the MVME147-01x and MVME147-02x MPU VMEmodules. References herein to the MVME147 apply to all models listed in Table 1-1 below, except where otherwise noted.

## Model Designations

The MVME147 is available in several configurations which are summarized in the following table. The main differences between the versions are processor speed and memory size.

**Table 1-1. MVME147 Model Designations**

Model Number	Clock Speed	Memory	Parity	Ethernet
MVME147-010	16 MHz	4MB	No	No
MVME147-011	25 MHz	4MB	Yes	Yes
MVME147-012	25 MHz	8MB	Yes	Yes
MVME147-013	25MHz	16MB	Yes	Yes
MVME147-014	25 MHz	32MB	Yes	Yes
MVME147-022	32 MHz	8MB	Yes	Yes
MVME147-023	32 MHz	16MB	Yes	Yes
MVME147-024	32 MHz	32MB	Yes	Yes

### Note

The memory maps differ for the 4, 8, 16, and 32MB versions (refer to the *Memory Map* section in Chapter 3).

## Features

The features of the MVME147 include:

- MC68030 microprocessor
- Floating-Point Coprocessor (MC68882)

- Shared DRAM with parity (no parity on MVME147-010)
- Four serial ports with EIA-232-D buffers
- Small Computer Systems Interface (SCSI) bus interface with DMA channel
- Time-of-day clock/calendar with battery backup
- CMOS RAM, 2K by 8 with battery backup or equivalent
- Four ROM/PROM/EPROM/EEPROM sockets (organized as 16 bits wide)
- VMEbus interrupter
- VMEbus system controller functions
- VMEbus master interface (A32/D32, A24/D16 compatible)
- VMEbus requester
- Status LEDs for SCON, DUAL, FAIL, and STATUS
- RESET and ABORT switches
- Centronics printer port
- Two 16-bit tick timers for periodic interrupts
- Watchdog timer
- Ethernet transceiver interface (except MVME147-010)
- General Control Chip (GCC)

## Specifications

The MVME147 specifications are given in Table 1-2.

**Table 1-2. MVME147 Specifications**

Characteristics	Specifications
Power requirements (MVME147 with two EPROMs and MVME712M) Note: Power must be brought in from both the P1 and P2 backplanes or connectors P1 and P2	+5 Vdc, 4.5A maximum (3.5 A typical) +12 Vdc, 100mA maximum (100 mA maximum - no LAN) -12 Vdc, 100 mA maximum
Microprocessor	MC68030
Clock signal	16/25/32 MHz to MPU and FPC (depends on version)