

Weave AG voltages (page 1 of 2)

MOS				
Head 20-41	Voltage	Function	CCD pin	SDSU 20-41
S	0v	clock screen		S
r	2v,-10v	I1	7	r
s	2v,-10v	I2	6	s
k	2v,-10v	I3	5	k
m	2v,-10v	S1	30	m
n	2v,-10v	S2	31	n
t	2v,-10v	S3	32	t
g	-8v,1v	R1L	18	g
R	-8v,1v	R2L	17	R
h	-8v,1v	R3L,R3R	16,21	h
q	-8v,2v	PhiRR,PhiRL	22,15	q
N	0v	VSS,Pre-amp gnd,ABD	3,8,14,23,29,34,33	N
e	8v	RDL,RDR	10,27	e
J	-6v	OG	9	J
P	-10v,2v	DG	28	B
d	21v	ODR,ODL,ABD	24,13,4	d
f	0v	Sheath_spare (right)	right pre-amp sheath	f
c	0v	spare		Z
L	0v	Amp_MOS (left)	left pre-amp out	L
M	0v	Amp_spare (right)	right pre-amp out	M
K	0v	Sheath_MOS (left)	left pre-amp sheath	K
p	0v	analog screen		p
A	0v	Chassis		A
T	0v	ID return		T
W	0v	ID1		W
X	0v	ID2		X
Y	0v	ID3		Y
Z	0v	Pre-flash enable		i
i	-8v,1v	R1R	19	U
j	-8v,1v	R2R	20	V
E	0v	heatsink temp-		E
F	0v	heatsink temp+		F
D	0v	MOS Peltier status		a
G	9v	pre-amp power +		G
a	0v	MOS CCD temp	CCD temp op-amp output	j
b	0v	spare		b
H	-9v	pre-amp power -		H
B	5v	Pelt +		
C	0v	Pelt -		
U	12v	Fan +		
V	0v	Fan -		
		temp screen		D

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LIFU				
Head 20-41	Voltage	Function	CCD pin	SDSU 20-41
S	0v	clock screen		S
r	2v,-10v	I1	7	r
s	2v,-10v	I2	6	s
k	2v,-10v	I3	5	k
m	2v,-10v	S1	30	m
n	2v,-10v	S2	31	n
t	2v,-10v	S3	32	t
g	-8v,1v	R1L	18	g
R	-8v,1v	R2L	17	R
h	-8v,1v	R3L,R3R	16,21	h
q	-8v,2v	PhiRR,PhiRL	22,15	q
N	0v	VSS,Pre-amp gnd,ABD	3,8,14,23,29,34,33	N
e	8v	RDL,RDR	10,27	e
J	-6v	OG	9	J
P	-10v,2v	DG	28	B
d	21v	ODR,ODL,ABD	24,13,4	d
f	0v	Sheath_spare (right)	right pre-amp sheath	f
c	0v	spare		Z
L	0v	Amp_LIFU (left)	left pre-amp out	L
M	0v	Amp_spare (right)	right pre-amp out	M
K	0v	Sheath_LIFU (left)	left pre-amp sheath	K
p	0v	analog screen		p
A	0v	Chassis		A
T	0v	ID return		T
W	0v	ID1		W
X	0v	ID2		X
Y	0v	ID3		Y
Z	0v	Pre-flash enable		i
i	-8v,1v	R1R	19	U
j	-8v,1v	R2R	20	V
E	0v	heatsink temp -		E
F	0v	heatsink temp +		F
D	0v	LIFU Peltier status		a
G	9v	pre-amp power +		G
a	0v	LIFU CCD temp	CCD temp op-amp output	j
b	0v	spare		b
H	-9v	pre-amp power -		H
B	5v	Pelt +		
C	0v	Pelt -		
U	12v	Fan +		
V	0v	Fan -		
		temp screen		D