

MITMosaic Two Chip Mosaic Camera:

Wiring internal to cryostat

Design compatible with WHTWFC mosaic controller and external cable

	cryo	chip x	chip y
CCD Sig	20-41	Nanonics pin no.	Nanonics pin no.
P1Ux,P1Lx	H	2,7,31,36	
P1Uy,P1Ly	g		2,7,31,36
P2Ux,P2Lx	P	1,6,32,37	
P2Uy,P2Ly	K		1,6,32,37
P3Ux,P3Lx	k	3,4,34,35	
P3Uy,P3Ly	M		3,4,34,35
S1Ax,S1Ay,S2Bx,S2By	G	18,21	18,21
	R		
SCUPPERx	j	8,30	
SCUPPERy	b		8,30
S2Ax,S2Ay,S1Bx,S1By	J	17,20	17,20
	N		
	h		
S3x,S3y	L	19	19
	f		
SWAx,SWBx	F	15,23	
SWAy,SWBy	S		15,23
	n		
	e		
RGABx	E	11,27	
RGABy	T		11,27
	p		
	Z		
SUBx,RETAx,RETBx	Y	16,22,14,24	
OGAx,OGBx	D	9,29	
OGAy,OGBy	m		9,29
RDAx	C	10	
RDBx	V	28	
RDAy	r		10
RDBy	X		28
DRAx	B	12	
OUTAx	A	13	
DRBy	U		25
OUTBy	W		26
DRAy	q		12
OUTAy	s		13
DRBx	c	25	
OUTBx	a	26	
SUBy,RETAy,RETBBy	i		16,22,14,24
N/C			

SMT July 2002

For pinout info on the nanonics connector see:

<http://www.ing.iac.es/~smt/redsense/ccid20.pdf>