

## EEV 10 Voltages

These are the voltages you can expect on the front panel of the SDSU controller and the 20-41 cryostat connector

CCD 21 way micro-D	flanged 20-41	Function	SDSU controller Mixed D connector	Voltage		
				Bias	Clock Hi	Clock Lo
1	F	SWL	1		1	-7.6
2	j	Dump Gate	3		3	-9
3	E	Phi-R Left	4		3	-6
4	J	H2L	6		1	-7.6
5	G	H1L	5		1	-7.6
6	L	H3	7		1	-7.6
7	R	H1R	6		1	-7.6
8	N	H2R	5		1	-7.6
9	T	Phi-R Right	4		3	-6
10	j	Dump Gate	3		3	-9
11	S	SWR	1		1	-7.6
12	m	OG1	2	-6		
13	Y	VSS	12	0		
14	K	V2	9		2	-9
15	g	V1	8		2	-9
16	M	V3	17		2	-9
17	n/c					
18	n/c					
19	n/c					
20	Y	VSS	12	0		
21	m	OG1	2	-6		
<b>CCD 15 way micro-D</b>						
1	q	Dump Drain	14	13		
2	C	RD	15	8.5		
3	D	OG2	16	-5		
4	n/c					
5	n/c					
6	D	OG2	16	-5		
7	V	RD	15	8.5		
8	q	Dump Drain	14	13		
9	c	OD Left	13	20		
10	a	OS Left	<b>A2</b>			
11	Y	VSS	12	0		
12	Y	VSS	12	0		
13	Y	VSS	12	0		
14	W	OS Right	<b>A3</b>			
15	U	OD Right	11	20		
			10	0		